

U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	Not Yet Assigned
		Filing Date	Not Yet Assigned
		First Named Inventor	HALBE HAGEMAN
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
SHEET	1	OF	1
		Attorney Docket Number P19058-US1	

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FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date mm-dd-yyyy	Name of Patentee or Applicant of Cited Document	Translation Y/N
		GB 2171541 A	08-28-1986	INFOQUEST CORP	

NONPATENT LITERATURE DOCUMENT				
Examiner's Initials	Cite No.	Include name of the author (In CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T
		LAJOLO ET AL: "Early evaluation of bus interconnects dependability for system-on-chips designs"; IEEE CONFERENCE PROCEEDINGS ARTICLE 3 January 2001 (2001-01-03) pages 371-376 XP010531481 PAGE 371-PAGE 373		
		VRANESIC ET AL: "Hector-a hierarchically structured shared memory multiprocessor"; IEEE CONFERENCE PROCEEDINGS ARTICLE; VOL. i, 8 January 1991 (1991-01-08), pages 444-453, XP0100093276		

Examiner Signature	/Glenn Auve/	Date Considered	02/24/2009
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /GA/